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**OSTROLENK, FABER, GERB & SOFFEN, LLP**  
**Attorneys at Law**  
**1180 Avenue of the Americas**  
**New York, New York 10036-8403**

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**Tel (212) 382-0700 Fax (212) 382-0888**  
**email@ostrolenk.com**



Express Mail #EV342534392US

Mail Stop Patent Application  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

OFGS File No. : P/126-224  
 Inventor : Yutaka ITO, et al.  
 Title : SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
 Assignee :

Enclosed herewith please find the following documents in the above-identified application for United States Letters Patent:

- ☒ Print EFS Data Sheet
- 55 Pages of Specification including Abstract and Claims
- 12 Numbered Claims Calculated as \_ Claims for Fee Purposes
- 32 Sheets of Drawing Containing Figures 1 to 33. (Formal)
- Declaration and Power of Attorney
- ☒ Priority is Claimed under 35 U.S.C. §119:  
 Convention Date October 30, 2002 for Japanese Appln. S.N. 315319/2002
- Certified Priority Application
- Small Entity Status is claimed.
- Assignment
- ☒ Return-Addressed Post Card

OFGS Check No. 13100, which includes the fee of \$770.00, calculated as follows:

Basic Filing Fee: .....	\$ 770.00
Additional Filing Fees:	
Total Number of Claims in Excess of 20, times \$18: .....	
Number of Independent Claims in Excess of 3, times \$86: .....	
One or More Multiple Dependent Claims: Total \$290: .....	
Total Filing Fees or .....	770.00
Total Filing Fee Reduced 50% for Small Entity: .....	
Assignment Recording Fee: \$40 .....	
TOTAL Filing Fee and Assignment Recording Fee: .....	<u>\$ 770.00</u>



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DOROTHY JENKINS

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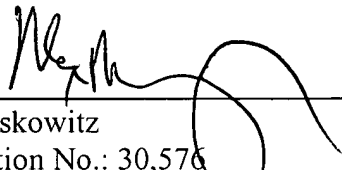


Signature

October 30, 2003

Date of Signature

Respectfully submitted,



Max Moskowitz

Registration No.: 30,576

OSTROLENK, FABER, GERB & SOFFEN, LLP

1180 Avenue of the Americas

New York, New York 10036-8403

Telephone: (212) 382-0700